

Figure 1 is a block diagram of a system architecture. The system includes a Host Processor (190) connected to a DSP(s) (160). The DSP(s) is connected to a LAN I/F (182) and an ATM Module. The LAN I/F is connected to a LAN (165) which includes Ethernet, FDDI, Wireless Ethernet, etc. The ATM Module is connected to an ATM25 LAN (180) via a Utopia interface. The DSP(s) is also connected to a Codec (175) and a DSP (170) which are part of an xDSL modem. The xDSL modem is connected to a WAN PORT. The Host Processor (190) is also connected to the DSP(s) via a Peripheral or Chip Interconnect.

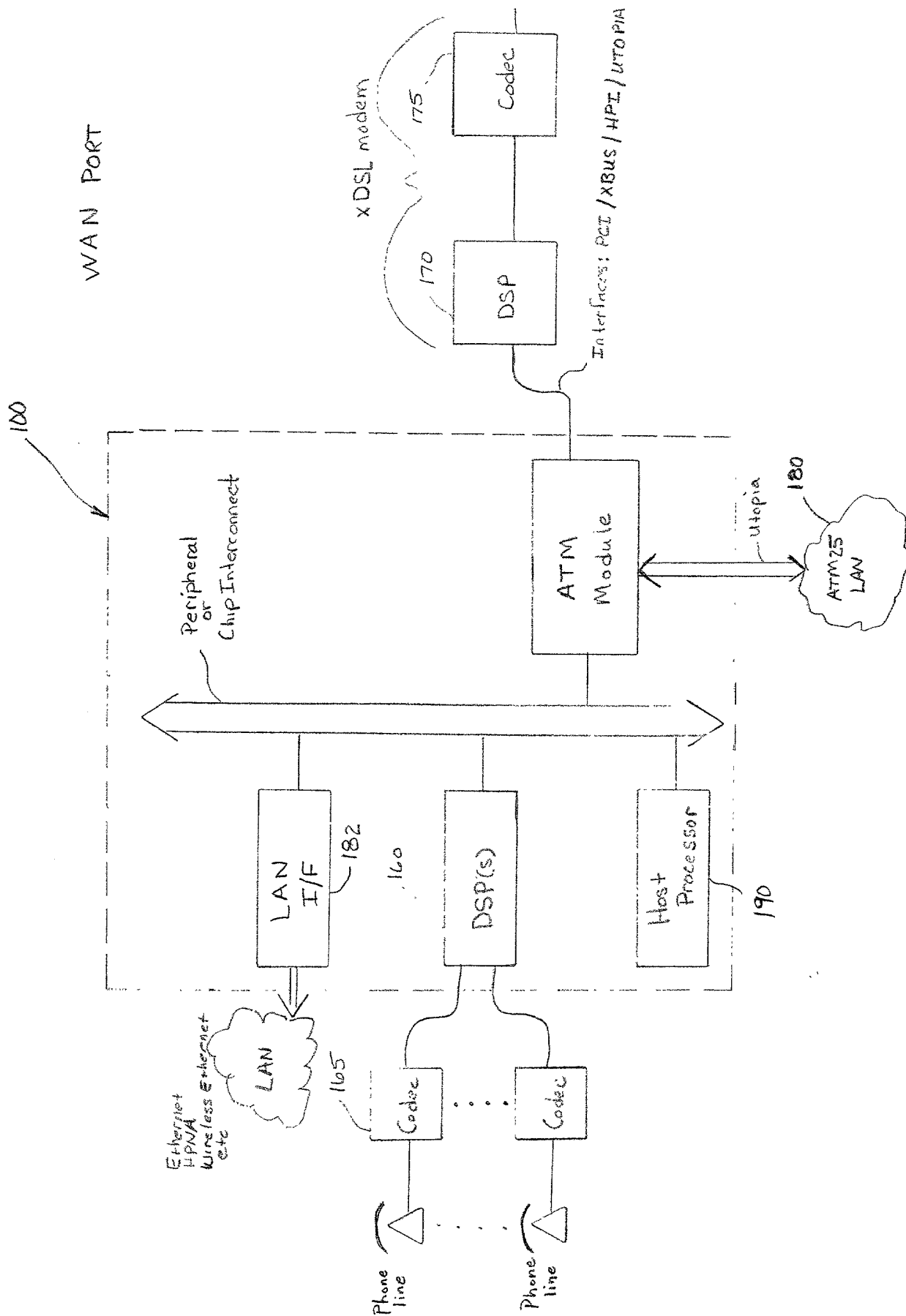


Figure 1

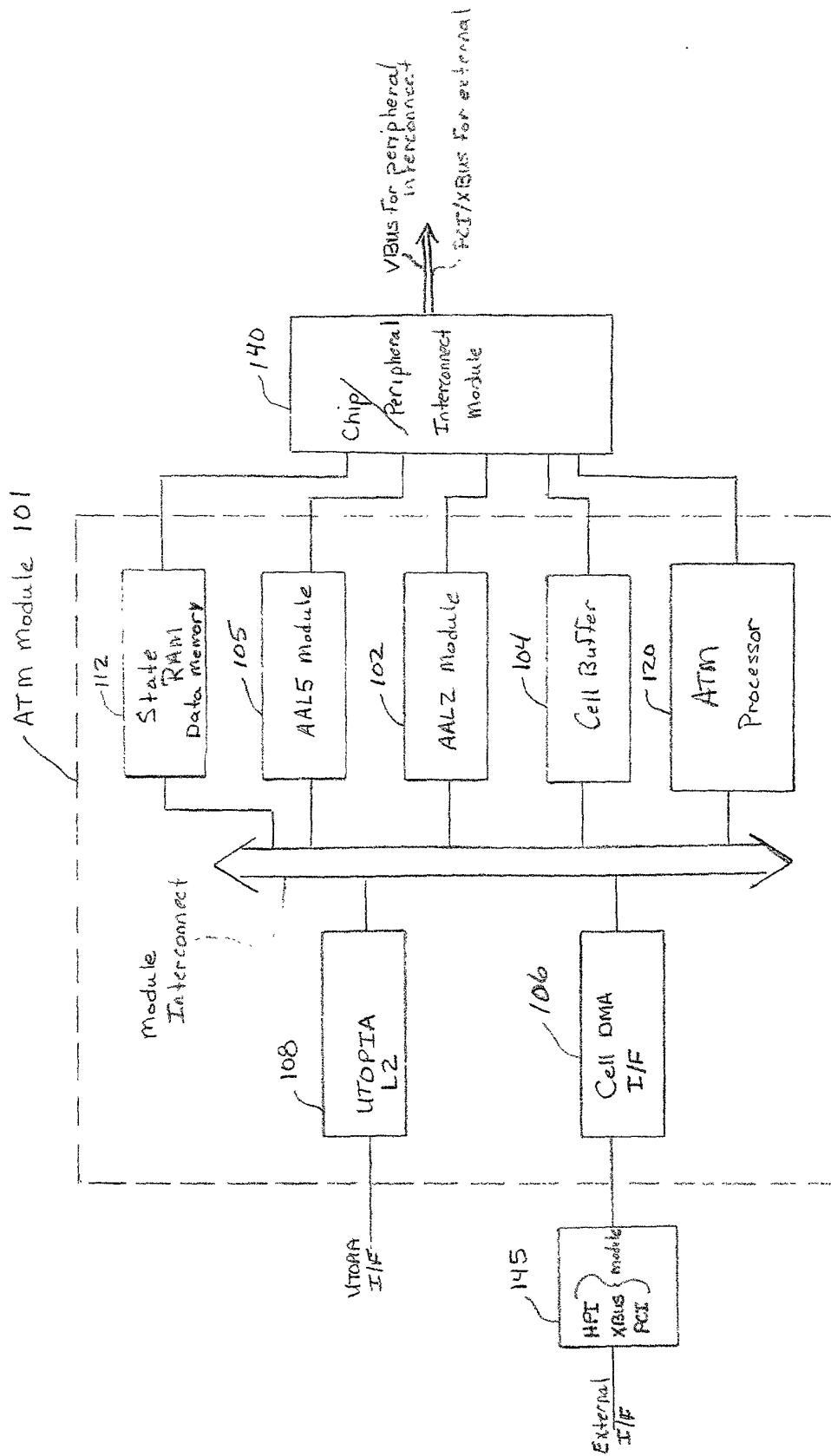


Figure 2

ATM Processor 120

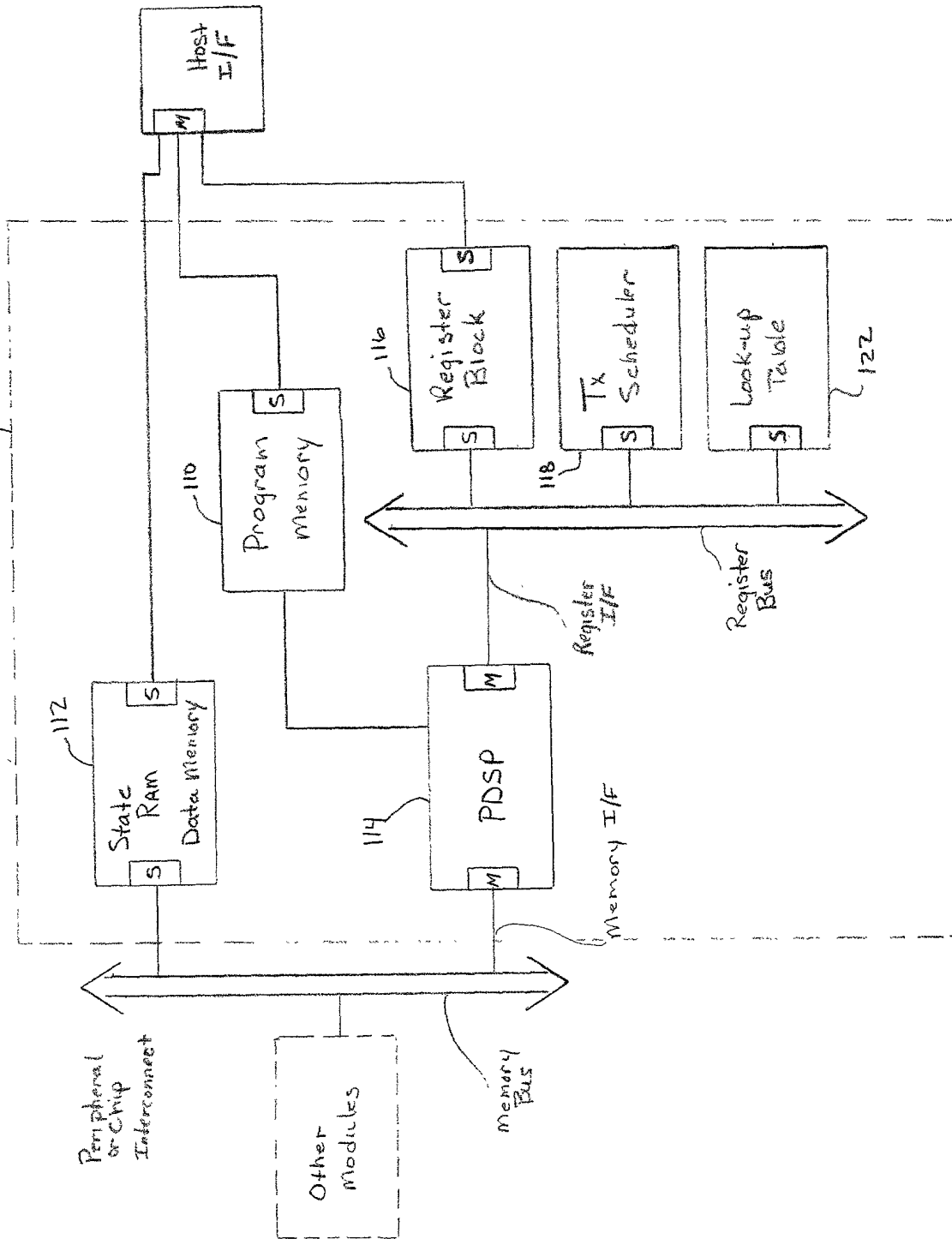


Figure 3

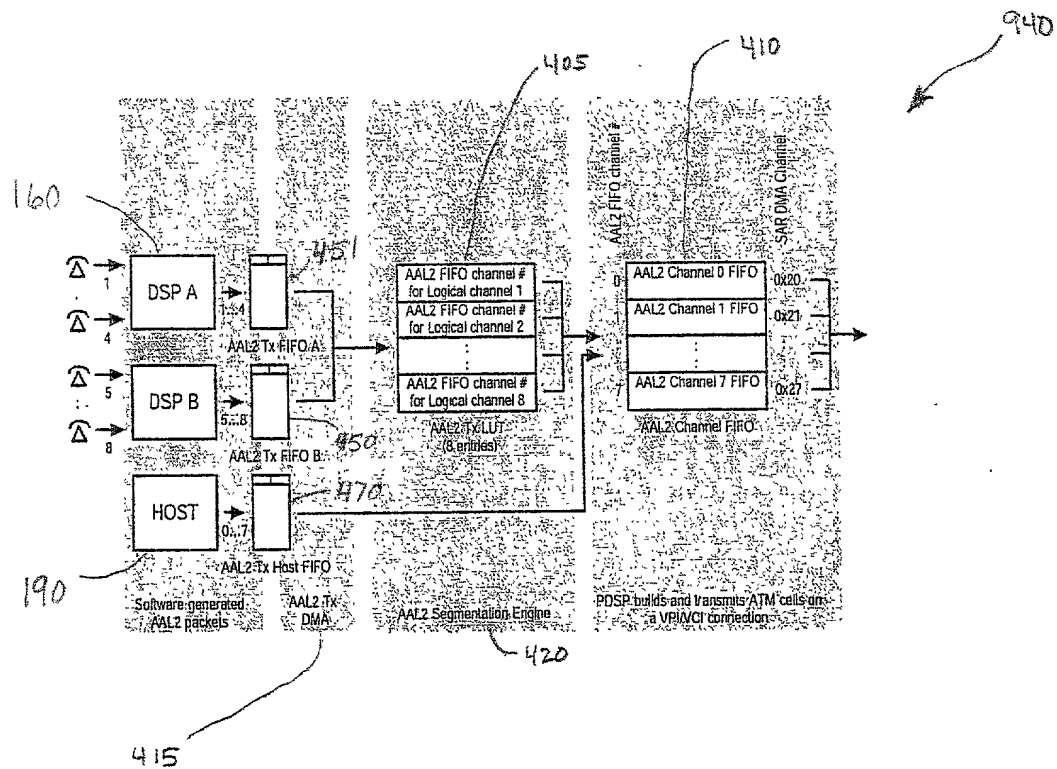


Figure 4

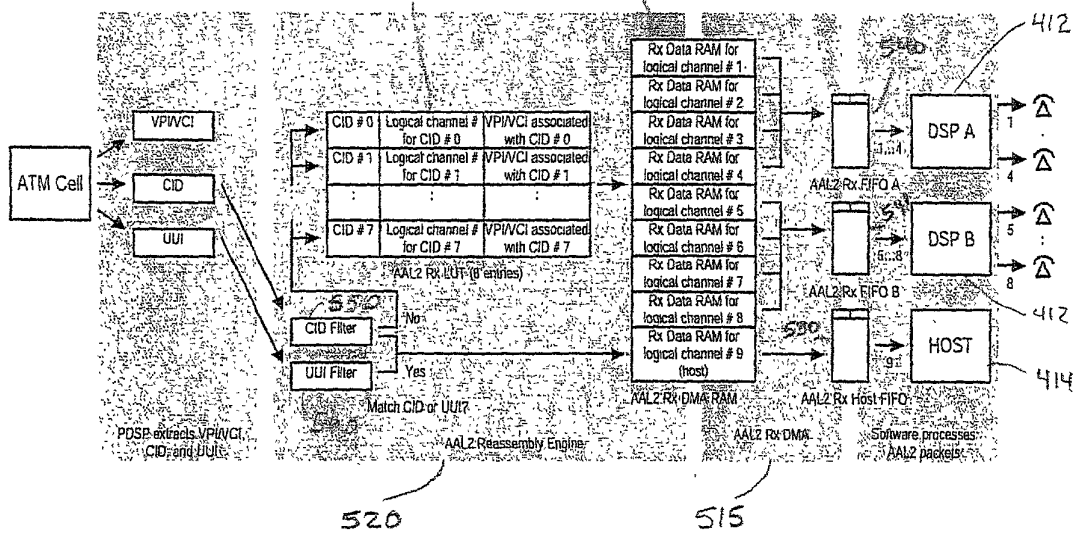


Figure 5

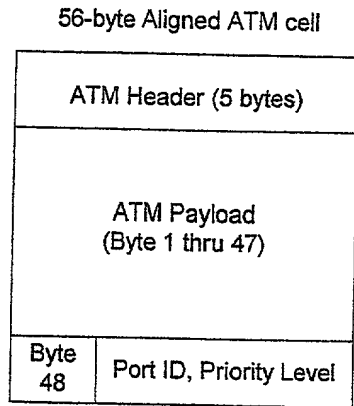


Figure 7

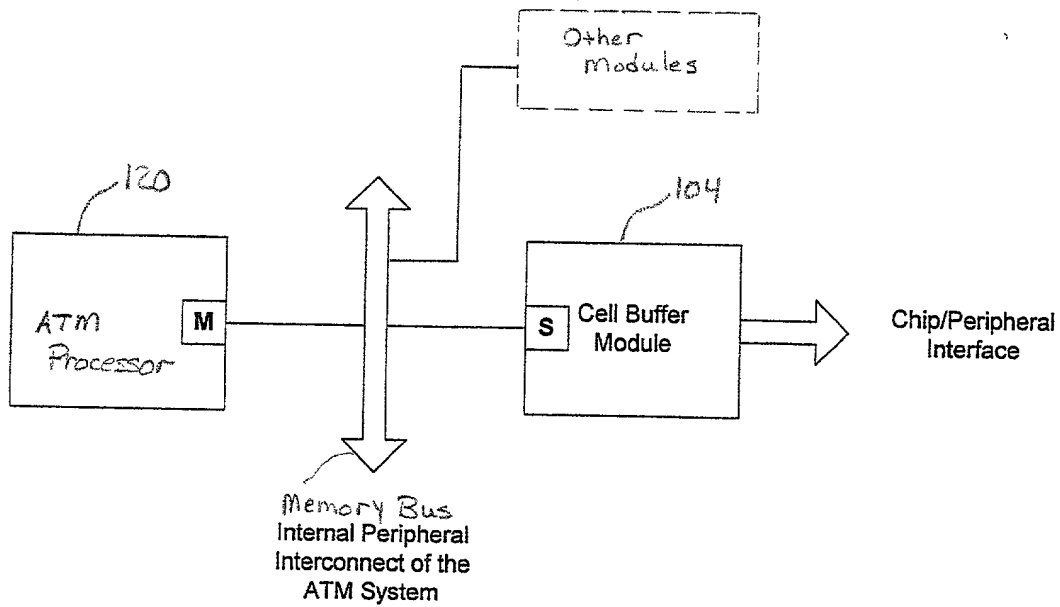


Figure 6A

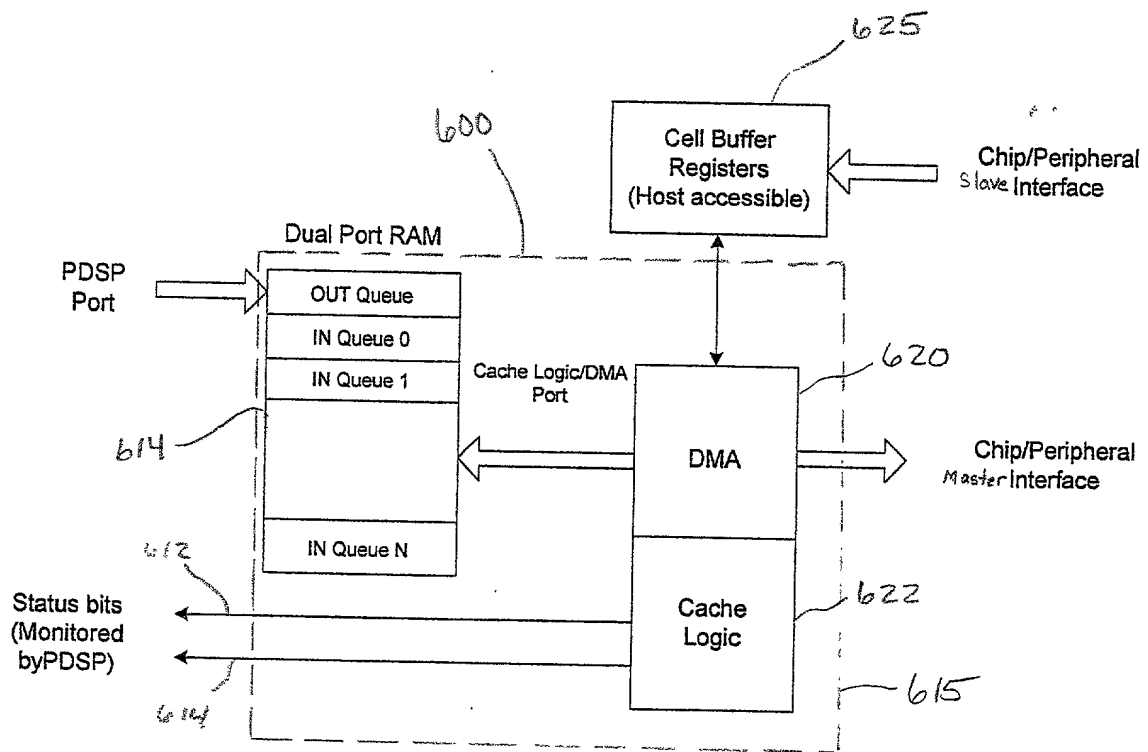


Figure 6B

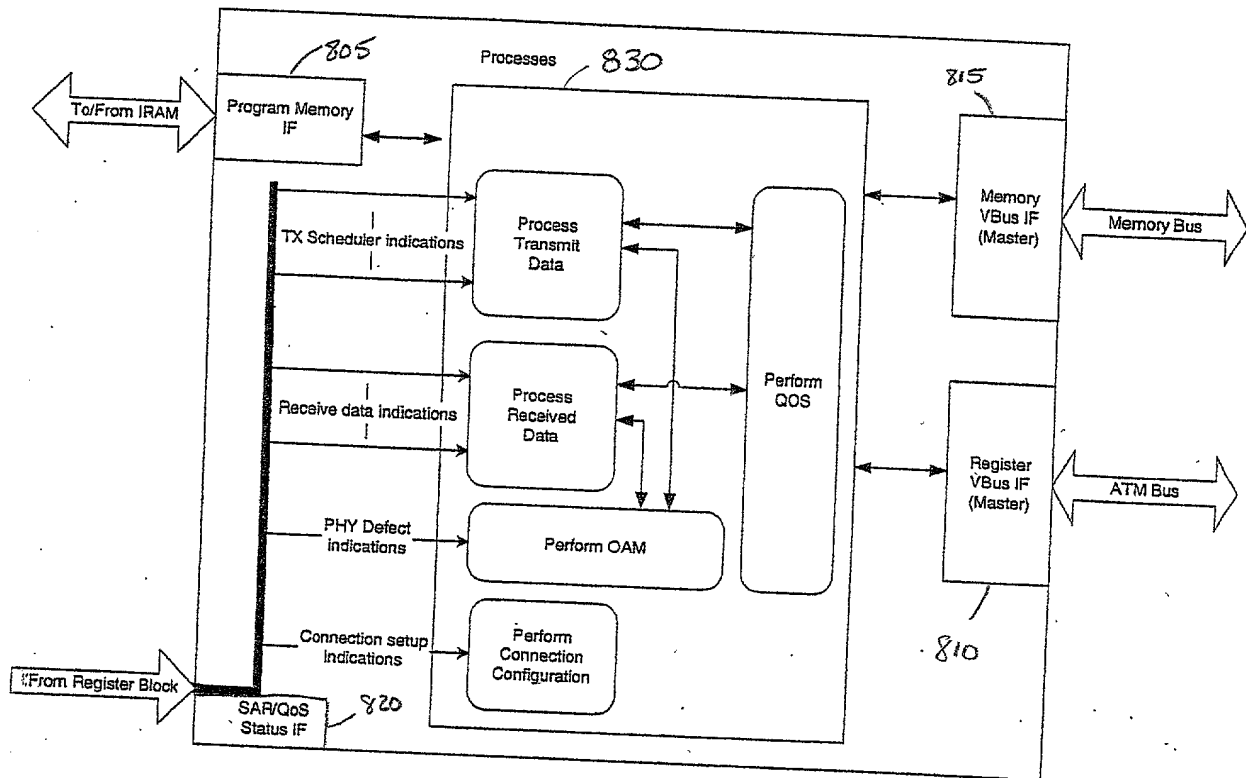


Figure 8